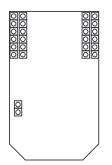
### **TECHNICAL DATA**

### **BUS INTERFACE MODULES** BIM M130, BIM M131, BIM M132, BIM M135



### **Features**

- EIB and KNX Bus Interface Module for piggyback-use on PCBs
- NEC 78K0/Kx2 microcontroller, contains the KNX System 2 stack and application
- PEI (Physical External Interface), one 8-bit I/O port and two PWMs for applications available
- User Flash memory
- User RAM
- Same pin-out as BIM M111/115 and M113
- Operating Temperature Range:
  - 5 to + 45 °C (M130, M131, M132)
  - 25 to + 70 °C (M135)
- Complies to KNX specification

### Description

The BIM M13x series of bus interface modules is based on the NEC 78K0/Kx2 microcontroller family providing state of the art flash memory technology. This enables the application designer to utilize a modern tool chain including debug tools leading to shorter software development times. In addition the BIM M13x series is designed with a high degree of hardware compatibility to BIM M111/115 and BIM M113 enabling to replace the BIM M111/113/115 in existing hardware in most cases reducing the development effort to the application software. As in BIM M111/113/115 the application interface includes the PEI, Reset, two PWMs and one 8-bit processor port. Additional pins to connect the external programming button and LED are available.

The BIM M13x modules contain the System 2 software compliant to the Konnex specification.

### **Order Numbers**

| Device   | Order Number   |  |  |  |  |
|----------|----------------|--|--|--|--|
| BIM M130 | 5WG1 130-8AA01 |  |  |  |  |
| BIM M131 | 5WG1 131-8AA01 |  |  |  |  |
| BIM M132 | 5WG1 132-8AA01 |  |  |  |  |
| BIM M135 | 5WG1 135-8AA01 |  |  |  |  |

#### **Absolute Maximum Electrical Ratings**

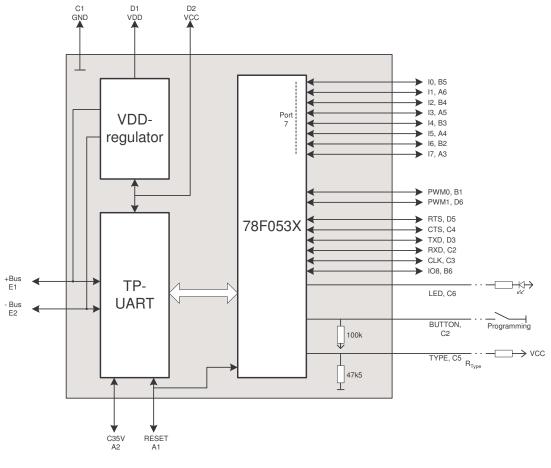
| Rating   | Symbol | Value                  | Units |
|--|--------|------------------------|-------|
| Bus Voltage  | V Bus  | ± 35                   | V     |
| Microcontroller Input Voltage PEI,<br>PWMs, Reset, LED, Button | V      | GND - 0,3 to Vcc + 0,3 | V     |

### Features of the Controller

- CPU NEC 78K0/Kx2
- 8-Bit A/D-converter API
- 8-Bit pulse length modulator (PLM)
- Serial asynchronous communication
- · Serial synchronous communication in software
- Input capture Interrupt available
- Output compare interrupt available
- Watch dog
- one 8-bit timer
- one 16-bit timer



### Hardware block diagram



Note: Use the C35V-pin only for extension of "bus buffer time". Do not draw current

Add capacitors to VCC, VDD and C35V to increase the "bus buffer time".

The following rule for a capacitor on VCC has to be obeyed:

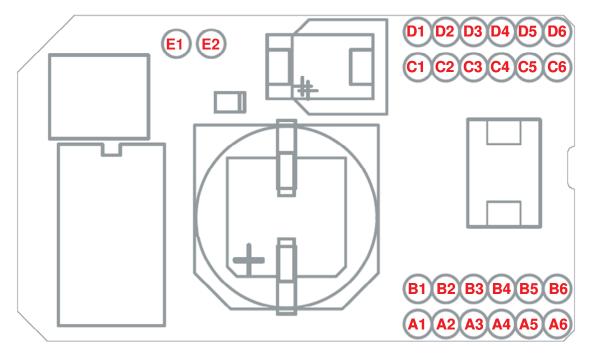
 $C_{VCC} < C_{C35} + 300 \mu F$ 

### **PEI (Physical External Interface)**

|            | BIM Pin<br>Mnemonics | μC-Pins<br>(BIM M113) | Input<br>Options (•)<br>(BIM M113)   | Output<br>Options(•)<br>(BIM M113) | μC-Pins<br>(BIM M13x) | Input<br>Options (•)<br>(BIM M13x) | Output<br>Options(•)<br>(BIM M13x) | Remarks   |
|------------|----------------------|-----------------------|--------------------------------------|------------------------------------|-----------------------|------------------------------------|------------------------------------|---|
| A1         | RESET                | _RESET                |                                      |                                    | _RESET                |                                    |                                    | In-/Output  |
| A2         | C35V                 |                       |                                      |                                    |                       |                                    |                                    | Buffer Capacitor  |
| A3         | 107                  | PA7                   |                                      |                                    | P77                   |                                    |                                    | Digital I/O   |
| <b>A</b> 4 | 105                  | PA5                   |                                      |                                    | P75                   |                                    |                                    | Digital I/O   |
| A5         | 103                  | PA3                   |                                      |                                    | P73                   |                                    |                                    | Digital I/O   |
| A6         | IO1                  | PA1                   |                                      |                                    | P71                   |                                    |                                    | Digital I/O   |
| B1         | PWM0                 | PC0                   | •AN0<br>•BEI06                       | •PWMA<br>•BEI06                    | P15                   | •ANI0                              | •TOH0                              | Digital I/O,<br>A/D-Converter,<br>Pulse-Width-Modulation                            |
| B2         | IO6                  | PA6                   |                                      |                                    | P76                   |                                    |                                    | Digital I/O   |
| <b>B</b> 3 | IO4                  | PA4                   |                                      |                                    | P74                   |                                    |                                    | Digital I/O   |
| B4         | IO2                  | PA2                   |                                      |                                    | P72                   |                                    |                                    | Digital I/O   |
| B5         | 100                  | PA0                   |                                      |                                    | P70                   |                                    |                                    | Digital I/O   |
| B6         | 108                  | PC7                   | •AN1                                 | •PWMA<br>•TCMPB                    | P01                   | •ANI1<br>•TI010                    | •TO00                              | Digital I/O,<br>A/D-Converter,<br>Capture,<br>Compare                               |
| C1         | GND                  |                       |                                      |                                    |                       |                                    |                                    | Ground  |
| C2         | BUTTON               | BEI05                 |                                      |                                    | P63                   |                                    |                                    | Only Digital In   |
| C3         | CLK                  | PC4                   | •AN6<br>•SPI-CLK<br>•TCAPB<br>•BEI07 | •SPI-CLK<br>•BEI07                 | P04                   | •ANI6<br>•SCK11                    |                                    | Digital I/O,<br>A/D-Converter,<br>(SPI-Clock-Out only in<br>Software)               |
| C4         | CTS                  | PC6                   | •AN3<br>•TCAPA                       | ●PWMA<br>●BEI01                    | P33                   | •ANI3                              | •TO51                              | Digital I/O,<br>A/D-Converter,<br>Pulse-Width-Modulation,<br>Clear to Send ∙←       |
| C5         | TYPE                 |                       | ●AN4                                 |                                    | P25                   | •ANI4                              |                                    | PEI-Type,<br>A/D-Converter  |
| C6         | LED                  | BEI05                 |                                      |                                    | P62                   |                                    |                                    | Port is an open drain   |
| D1         | VDD                  |                       |                                      |                                    |                       |                                    |                                    | 20V   |
| D2         | VCC                  |                       |                                      |                                    |                       |                                    |                                    | 5V  |
| D3         | TxD                  | PC3                   | •AN5<br>•SPI-MOSI                    | •SCI-TDO<br>•SPI-MOSI<br>•TCMPA    | P10                   | •ANI5<br>•SI11                     | •TxD0                              | Digital I/O,<br>A/D-Converter,<br>UART-TxD,<br>(SPI-Master-Out only in<br>software) |
| D4         | RxD                  | PC2                   | •AN7<br>•SCI-RDI<br>•SPI-MISO        | •SPI-MISO                          | P11                   | •ANI7<br>•RxD0                     | •SO11                              | Digital I/O,<br>A/D-Converter,<br>UART-RxD,<br>(SPI-Master-In only in<br>software)  |
| D5         | RTS                  | PC5                   | •AN2                                 | ●PWMB<br>●TCMPB                    | P00                   | •ANI2<br>•TI000                    |                                    | Digital I/O,<br>A/D-Converter,<br>Capture,<br>Request to Send ∙→                    |
| D6         | PWM1                 | PC1                   |                                      | •PWMB                              | P16                   |                                    | •TOH1                              | Digital I/O,<br>Pulse-Width-Modulation  |
| E1         | + Bus                |                       |                                      |                                    |                       |                                    |                                    | Bus Line  |
| E2         | - Bus                |                       |                                      |                                    |                       |                                    |                                    | Bus Line  |

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## **PIN Mapping**





### **Electrical Characteristics**

Complies to KNX Specification

Bus Interface Characteristics:

| Characteristics            | Symbol            | Min | Max | Typical | Unit  | Remarks   |
|----------------------------|-------------------|-----|-----|---------|-------|---|
| Operating Voltage          | V <sub>Bus</sub>  | 21  | 30  |         | V     |   |
| Current consumption        | I <sub>Bus</sub>  |     |     | 5,5     | mA    | V <sub>bus</sub> = 30 V<br>IDD = 0mA, ICC = 0mA |
| Reset activation condition |                   | 4.0 | 4.6 |         | V     | Reset generated by<br>transceiver. Conditions   |
|                            | V <sub>C35V</sub> | 5.8 | 6.2 |         |       | for active to reset state                       |
| Transmission Rate          |                   |     |     | 9600    | bit/s |   |

PEI DC-Characteristics:

| Characteristics               | Symbol | Min     | Тур | Max                      | Unit | Remarks  |
|-------------------------------|--------|---------|-----|--------------------------|------|--|
| Supply Output<br>Voltage +5V  | VCC    | 4.65    |     | 5.3                      | V    | Load ≤ 10mA                                    |
| Supply Output<br>Voltage +20V | VDD    | 17      | 19  | 23                       | V    | Load ≤ 5mA                                     |
| Supply current                | ICC    |         |     | 10                       | mA   | I <sub>PIN</sub> < 2,5mA<br>(source).          |
|                               | ICC    |         |     | 7,5<br>-I <sub>PIN</sub> |      | I <sub>PIN</sub> : sum of current on i/o pins. |
| Current limitation            | IDD    |         | 10  |                          | mA   | ICC ≤ 5mA                                      |
|                               | IDD    |         | 5   |                          | mA   | ICC = 10mA                                     |
| Data output voltage           | VOL    |         |     | 0.7                      | V    | Isink < 5mA                                    |
|                               | VOH    | VCC-0.7 |     |                          |      | Isource < 3mA                                  |
| Data input voltage            | VIL    |         |     | 0.2                      |      |  |
|                               | VIH    | 0.8 VCC |     | VCC                      |      |  |
| Analog input voltage          | VAIL   | 0       |     |                          |      |  |
| range                         | VAIH   |         |     | VCC                      |      |  |
| Input leakage                 | IL     |         | 1   |                          | uA   |  |
| current                       |        |         |     |                          |      |  |
| IO selectable pullup          |        | 10      | 20  | 100                      | kΩ   |  |
| Internal reset pullup         |        | 10      |     | 25                       | kΩ   |  |

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### Behavior on power loss

After a bus voltage breakdown for more than 1,5ms the save-routine of the bcu will be called. The time to reset will be sufficient to write a 64 byte data block to the internal flash memory if VCC, VDD and Pins have no load.

#### Software

The microcontroller of the BIM contains a System 2 stack in flash memory compliant with the KNX-Specification. An application program may be loaded via the bus. The development environment supports application software to be written in the C programming language. Note that application code written for BIM M111/113/115 cannot be used on BIM M13x. The available Flash and RAM space for the application program in the different BIMs are: 8 kbyte flash and 200 byte application and object ram BIM M130: BIM M131: 16 kbyte flash and 1.2 kbyte application and object ram BIM M132: 48 kbyte flash (banked) and 5,2 kbyte application and object ram BIM M135: 8 kbyte flash and 200 byte application and object ram Note: The necessary space for code with same functionality as in BIM M111/113/115 may vary because a microcontroller with a new architecture is used and the programming language is C instead of assembler.

### **Tool Chain**

The BIM M13x supports a state of the art tool chain including On-Chip Debugging (IAR Embedded Workbench).

### **Application Hint**

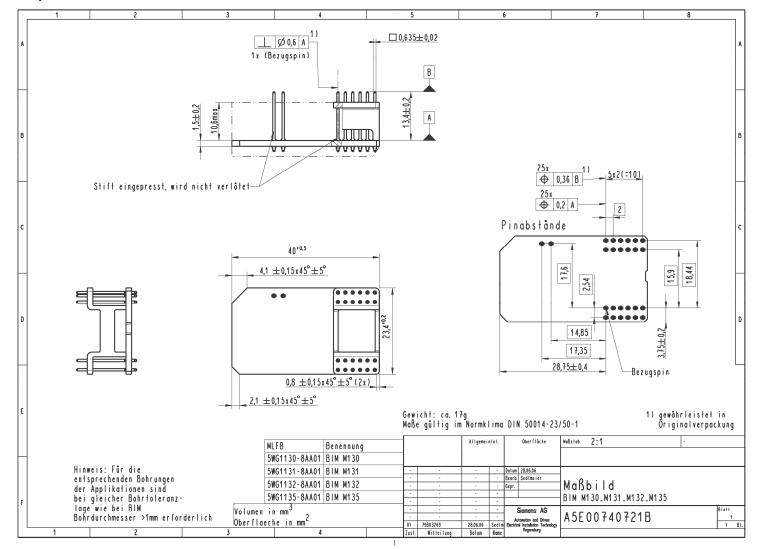
To avoid malfunctioning by EMI, it is recommended to shield the BIM. The connection for the electric screen is shown in the figure below.



Connection for electric screen (= Ground), for information about the exact position see "Maßbild"

### BIM M130, M131, M132, M135

**Mechanical Specification:** 



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